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FIG.1

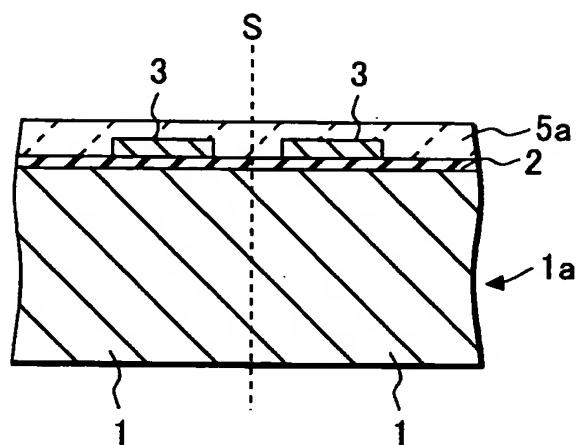
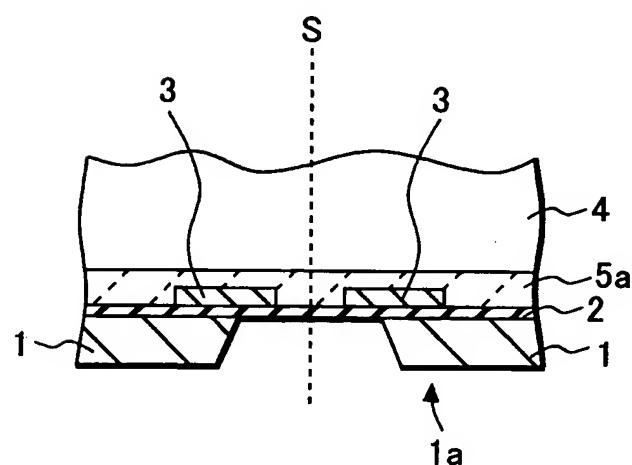


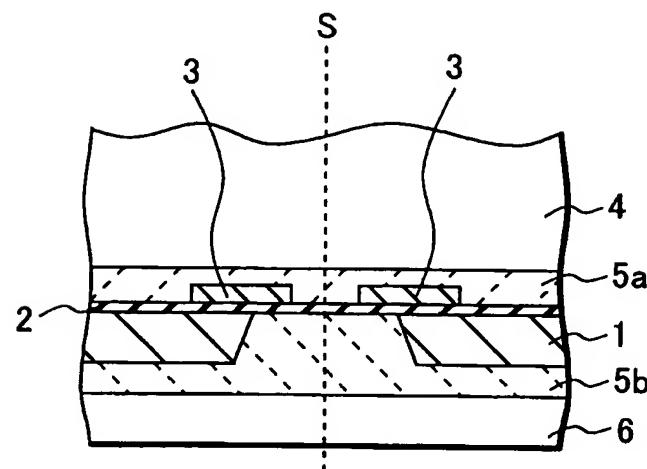
FIG.2



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FIG.3



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FIG.4A

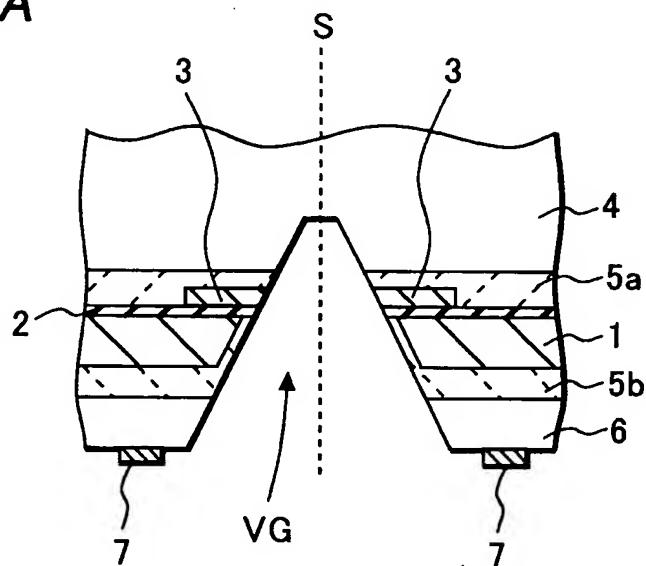
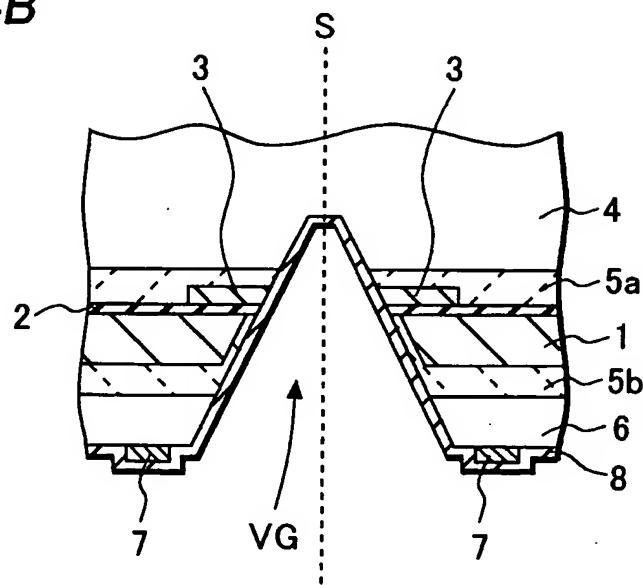


FIG.4B

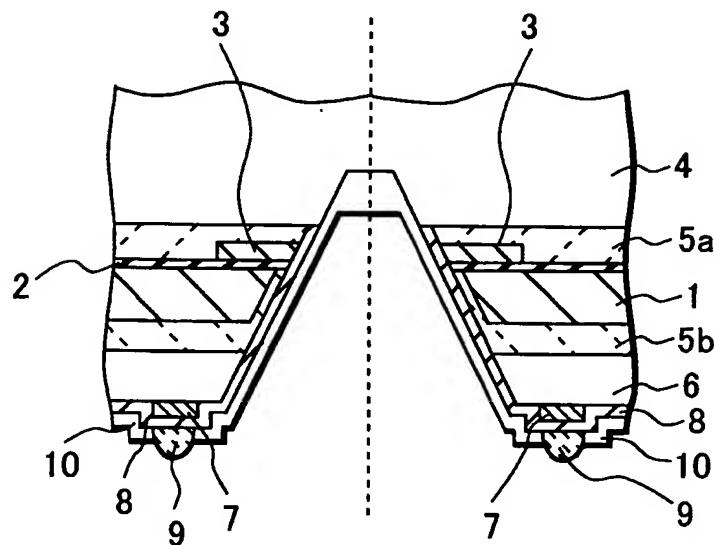


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Title: SEMICONDUCTOR DEVICE MANUFACTURING METHOD
Inventor(s): Takashi NOMA
Application No.: Unassigned
Docket No.: 492322015200

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FIG.5



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FIG.6A

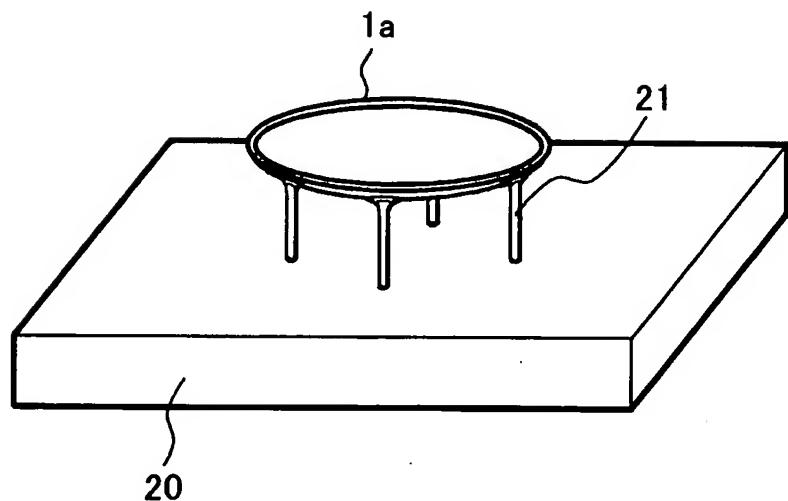
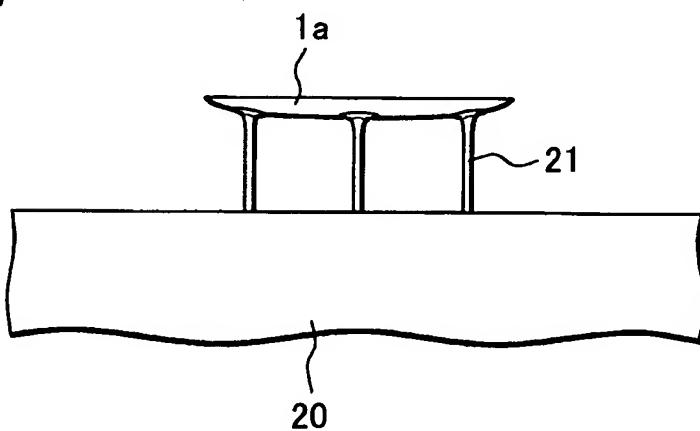


FIG.6B



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FIG. 7A

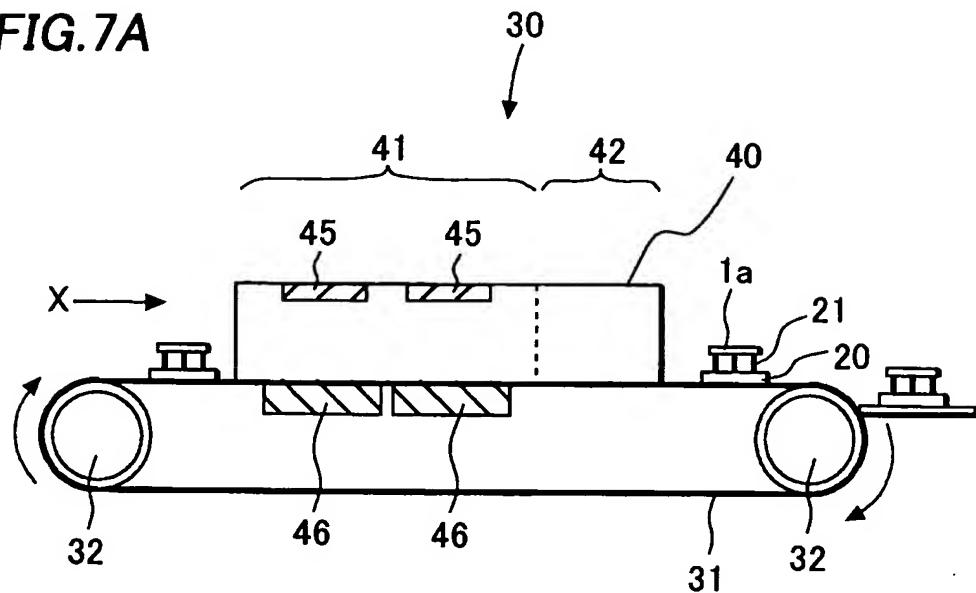
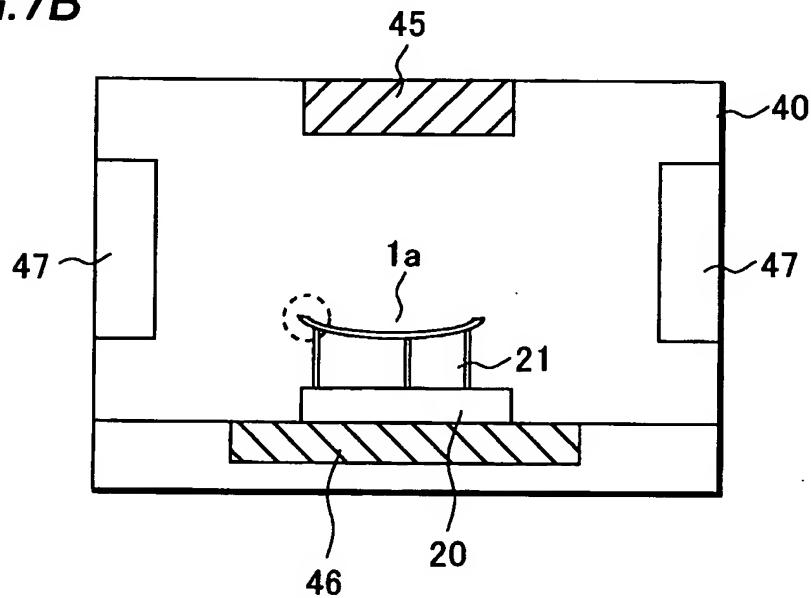


FIG. 7B



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FIG.8

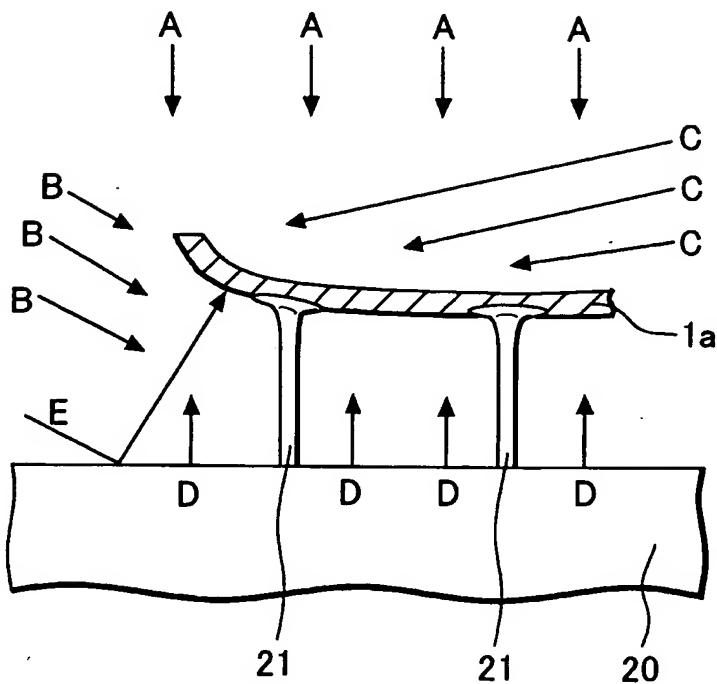
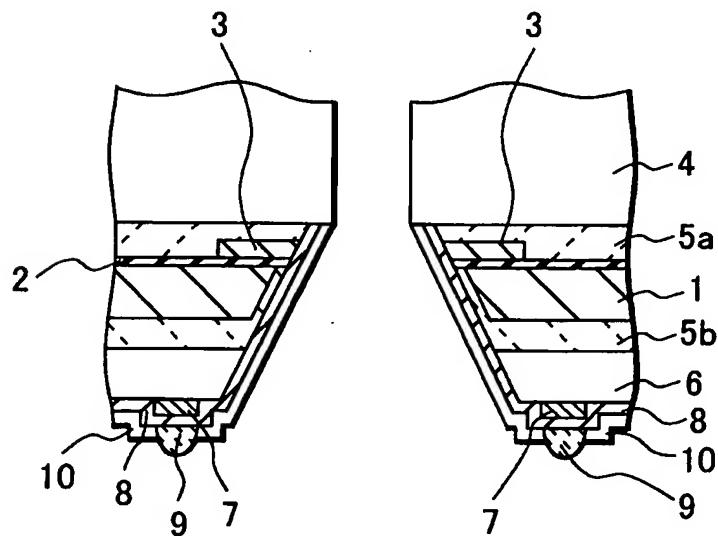


FIG.9



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FIG. 10A PRIOR ART

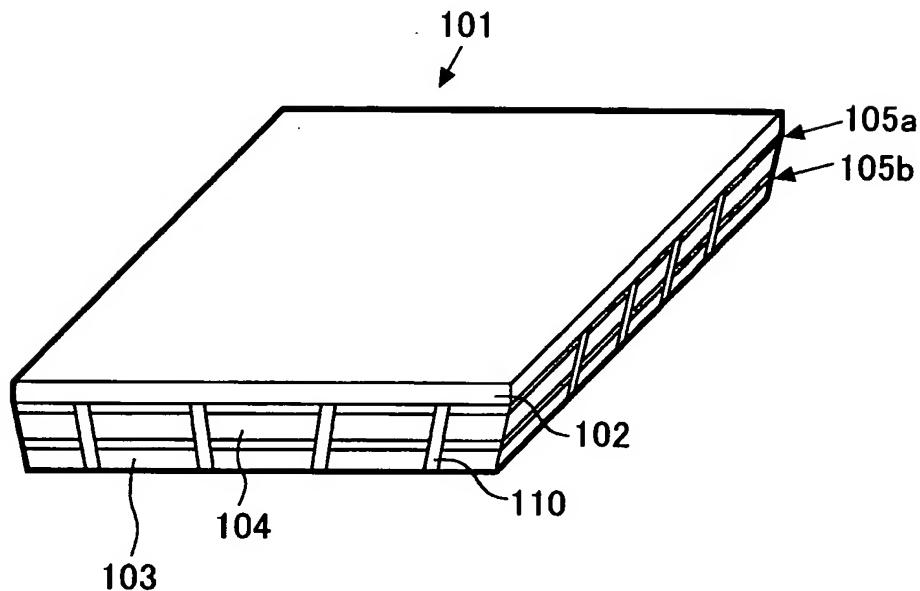
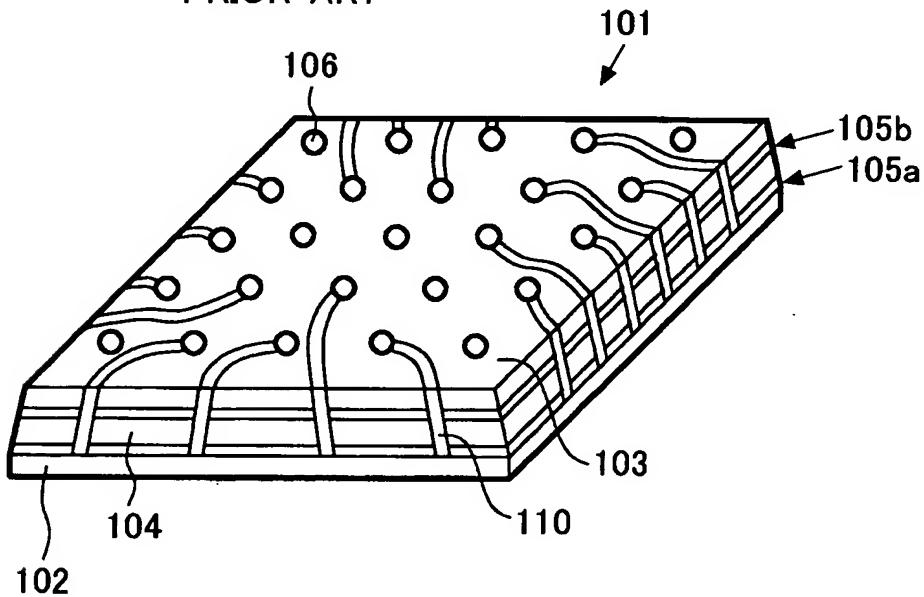


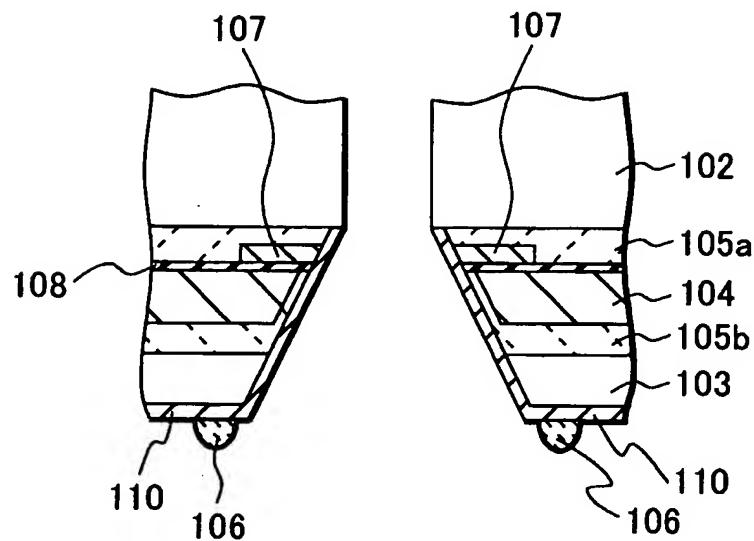
FIG. 10B PRIOR ART



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FIG.11
PRIOR ART



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FIG.12A PRIOR ART

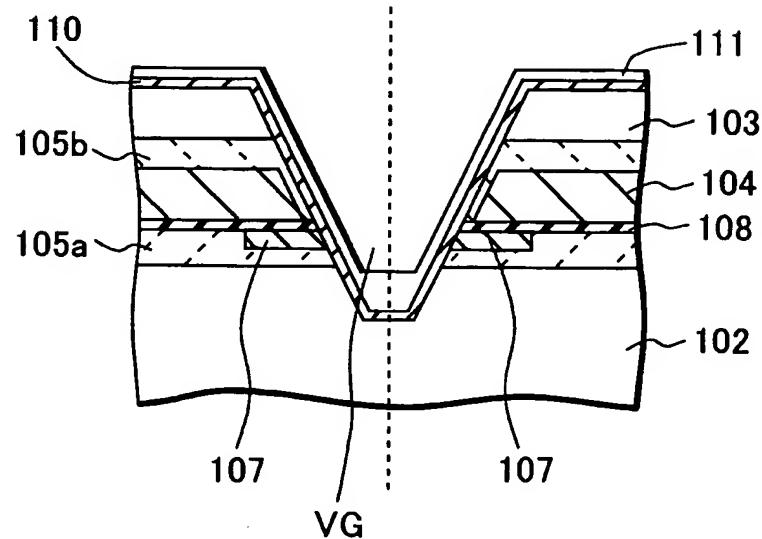
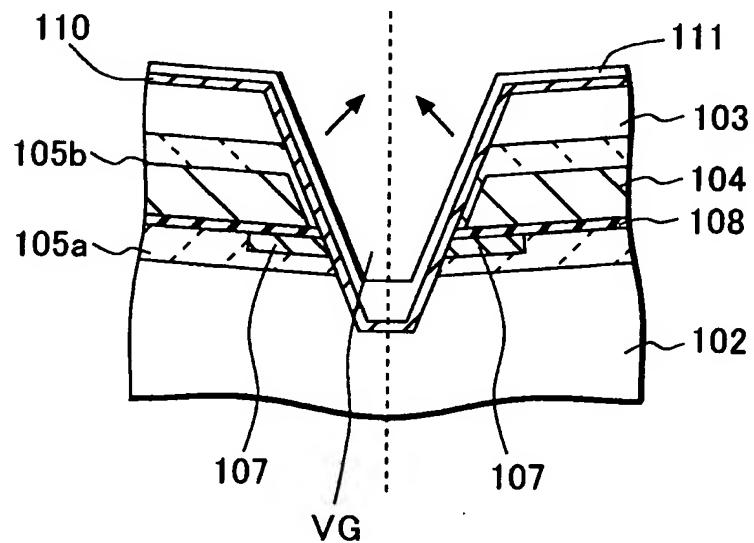


FIG.12B PRIOR ART



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FIG.13 PRIOR ART

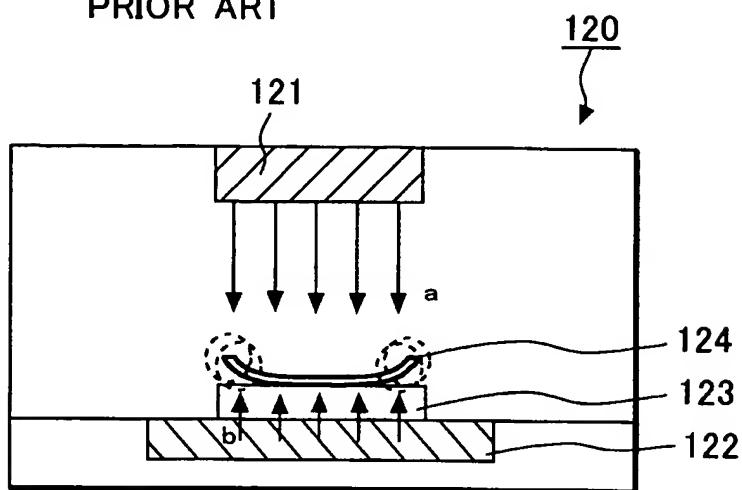
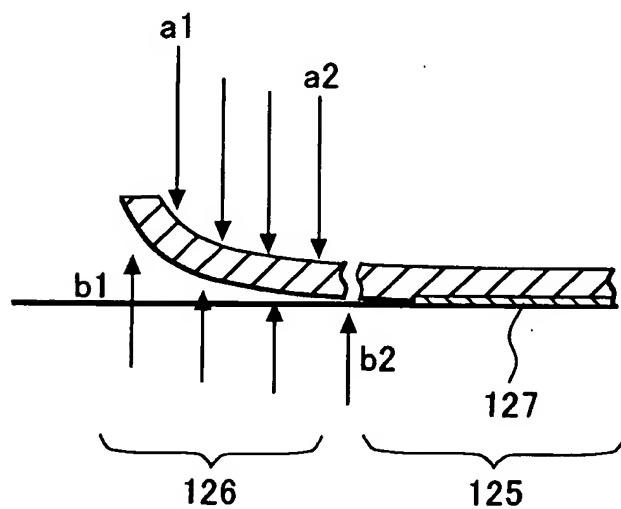


FIG.14 PRIOR ART



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